AMENDMENT   YOR920030249US1
March 20, 2006   Serial No. 10/644,210

AMENDMENTS TO THE SPECIFICATION:

Please replace the Abstract on page 18, lines 7 – 11 with the following amended Abstract.

A cache memory that may include ~~including~~ a content addressable memory, random access memory (CAMRAM) cache and method for managing a cache to reduce cache energy consumption. A cache buffer receives incoming data and buffers a storage array. The cache buffer holds a number of most recently accessed data blocks. In any access, cache buffer locations are checked before checking the storage array.

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